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## LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

2823

ATTY. DOCKET NO.: P68962US0 GROUP ART UNIT: Not Yet Assigned  
 SERIAL NO.: New Application 10/612,074 FILING DATE: July 3, 2003  
 APPLICANT(S): Jae Han CHA TODAY'S DATE: July 3, 2003

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## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
AA						
AB						

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## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AC						
AD						
AE						

\*\*\*\*\*

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

*ME* AF S. J. Ahn, et al.; "Novel DRAM Cell Transistor with  
Asymmetric Source and Drain Junction Profiles Improving Data  
Retention Characteristics"; 2002 Symposium On VLSI Technology  
Digest of Technical Papers; pp. 176-177.

EXAMINER Michelle Estrada DATE CONSIDERED 2/17/04

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).